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PATENT NUMBER

U.S. UTILITY Patent Application

PATENT DATE

APPLICATION NO. " 09/783821 CONT/PRIOR

CLASS SUBCLASS

ART UNIT 2133 EXAMINER

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Techniques for mitigating , detecting and correcting single event upset effects in systems using SRAM-based field programmable gate arrays

ISSUING CLASSIFICATION									
ORIGINAL		CROSS REFERENCE(S)							
CLASS SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL	. CLASSIFICATION	,							
			Continued on Issue Slip Inside File Jacket						

TERMINAL	DRAWINGS			CLAIMS ALLOWED	
└─ DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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subsequent to (date) has been disclaimed.	(Assistant Examiner) (Date)				
The term of this patent shall					
not extend beyond the expiration date of U.S Patent. No.				ISSUE FEE	
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